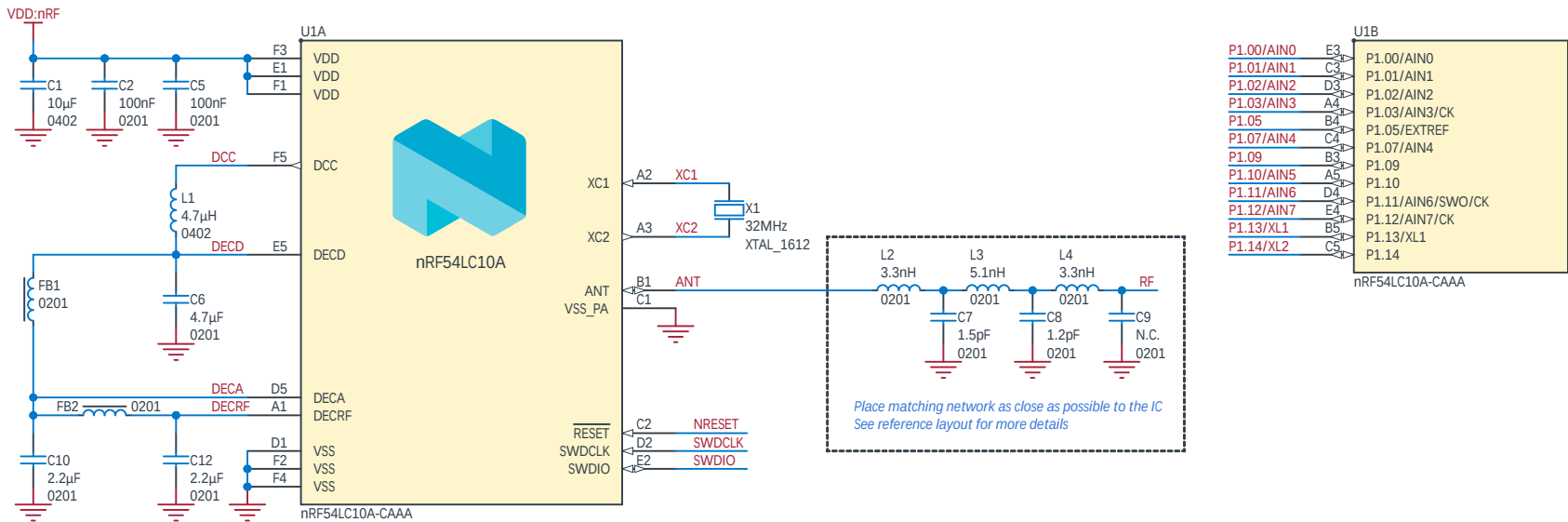


Reference Layout for nRF54LC10A-CAAA

Config 1: Enabled DC/DC - 6 Layer PCB

Reference layout revisions earlier than 1.0 can be changed without prior notice.
Always refer to Nordic Semiconductor documentation for the latest information
on recommended reference layout files before starting your design.



Nordic Semiconductor does not warrant the accuracy or completeness of this documentation or any included information. Nordic Semiconductor makes no assurances that this design will meet specification, be appropriate for your application, or fit for any particular purpose, or that it will function in an implementation. Nordic Semiconductor also does not claim that the design is ready for production. It is your responsibility to thoroughly validate and test your design implementation to confirm the functionality of your application.
© Nordic Semiconductor — All rights reserved.

Title			
Enabled DC/DC - 6 Layer PCB			
Size	Config. No.	Device	Revision
A4	1	nRF54LC10A-CAAA	0.2
Date: 2026-09-15			Sheet 1 of 1
File: nRF54LC10A-CAAA_6L_config1.SchDoc			Drawn By: Nordic Semiconductor
Classification: Public			

